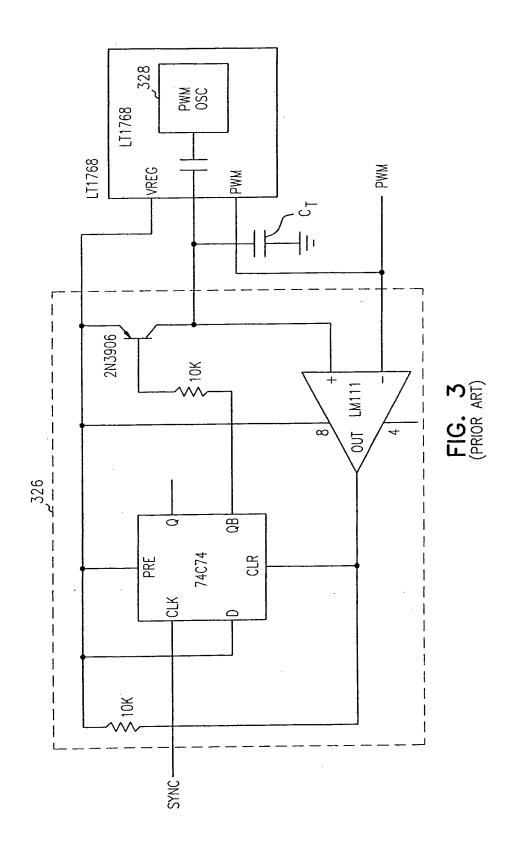
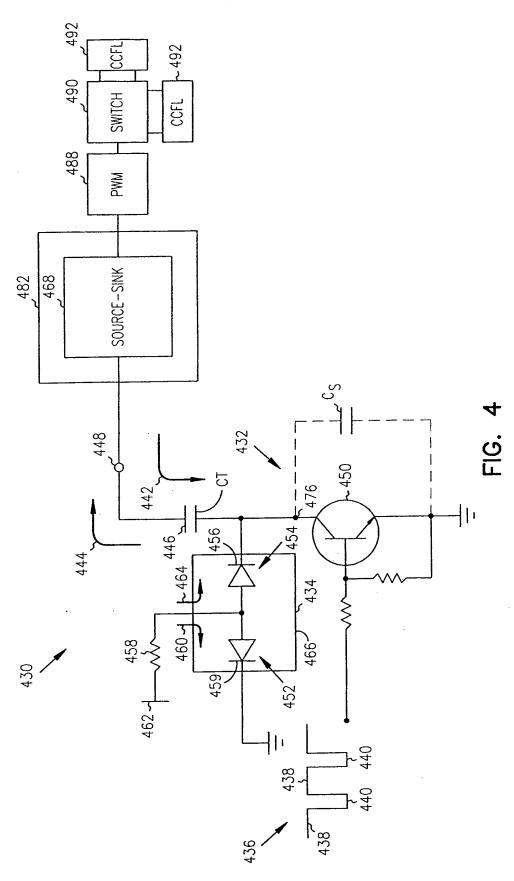
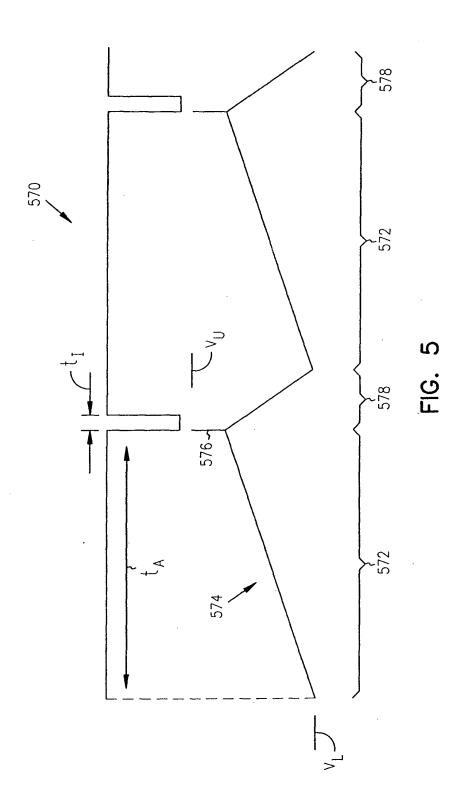
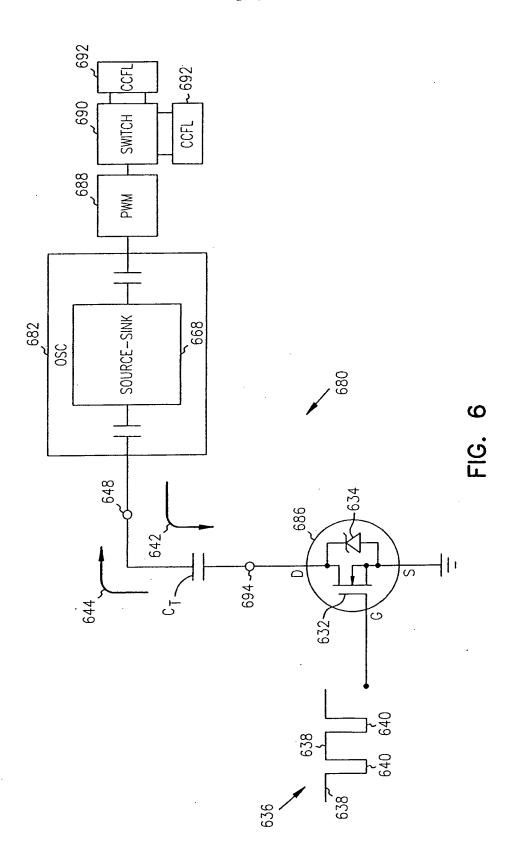


FIG. 2 (PRIOR ART)



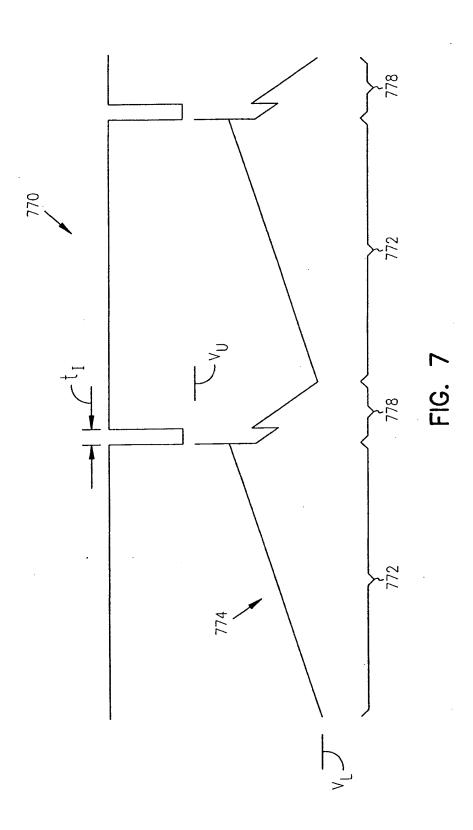






TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD INVENTORS NAME: Brian N. Poindexter

7/9



8/9

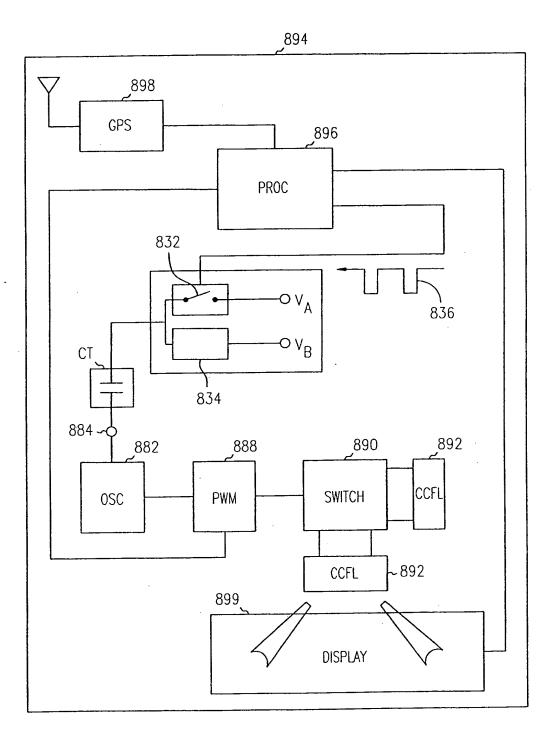


FIG. 8

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD INVENTORS NAME: Brian N. Poindexter

